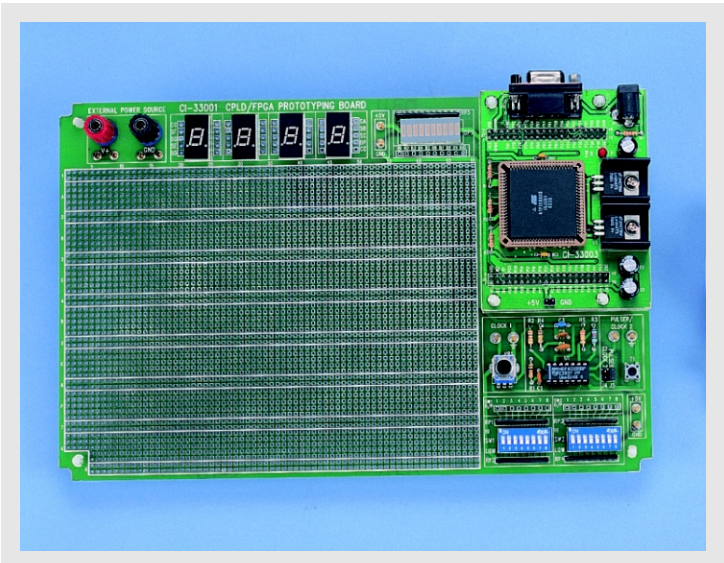
CI-33001C

CPLD/FPGA Prototyping Board



Prototyping Board

1. Adopt Atmel ATF1508-15 FPGA chip compatible with Altera MAX 7128, containing 128 Microcells over 2500 usable gates, with which it is able to reprogram over 10k times.
2. Using Altera MAX+PLUS® II for chip development. Users can use graphic or text editor (HDL syntax) to design, simulate and implement digital circuit easily.
3. The program is downloaded from PC to FPGA chip via series port with JTAG technology.
4. Providing some simple I/Os for design efficiency
5. Reserving large hardware design area best for circuit prototyping and student project implementation
6. Best solution for the shortage of budget

Specification

1. 16 DIP switches for digital state input
2. 10-BAR LEDs for output state display
3. 4-digit 7-segment display for static and dynamic driving operation
4. 2 channels clock pulse output
   Adjustable frequency range: 10Hz to 350Hz (±20%)
   fixed frequency: 3.5 KHz (±20%)
5. Altera MAX+PLUS®II
6. Altera file transfer software POF2JED and ISP programming software
7. For Windows 95/98/2000/XP

Accessories

1. ByteBlaster
2. Serial cable
3. Power adaptor
4. Operation manual