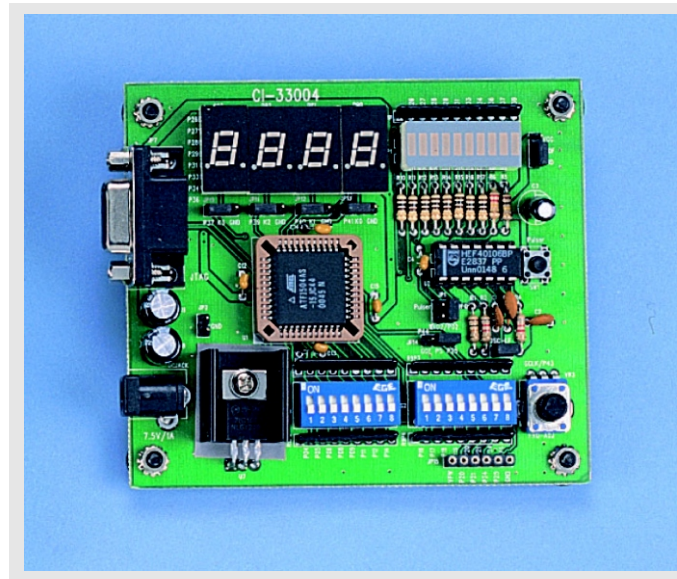# CI-33004

Experiment Board

Experiment Board

1. Atmel ATF1504-15 FPGA chip compatible with Altera MAX 7064, containing 64 Microcells over 1000 usable gates, with which it's able to reprogram about 10k times.
2. Using Altera MAX+PLUS®II for chip development. Either graphic or text editor (HDL syntax) can be used to design, simulate and implement digital circuit easily.
3. Through series port, the program is downloaded from PC to FPGA chip with JTAG technology.
4. Providing some simple I/Os for design efficiency
5. Suitable for new FPGA designers
6. Best solution for budget shortages

Specification

1. 16 DIP switches for digital state input
2. 10-BAR LEDs for output state display
3. 4-digit 7-segment display for static and dynamic driving operation
4. 2 channels clock pulse output
   Adjustable frequency range: 10Hz to 350Hz ($\pm$20%)
   fixed frequency: 3.5 KHz ($\pm$20%)
5. Altera MAX+PLUS®II
6. Altera file transfer software POF2JED and ISP programming software
7. For Windows 95/98/2000/XP

Application Fields

1. Design combinational logic circuits
2. Design sequential logic circuit
3. Design flip-flop circuit
4. Design counters and applications
5. Design ALUs and applications
6. Encoder/decoder and multipliers/demultipliers
7. Production and application of digital subjects

Accessories

1. ByteBlaster
2. Serial cable
3. Power adaptor
4. Operation manual